

09/08/01 Q60

L Number	Hits	Search Text	DB	Time stamp
1	295510	digital and analog	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:39
2	2654	(digital and analog) and (phase adj2 loop.ab.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:21
3	112	((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:22
4	87	((((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:22
5	78	((((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider)) not us.cc.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:23
6	23693	digital.ti. and analog.ti.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:04
7	138	(digital.ti. and analog.ti.) and (phase.ab. adj2 loop)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:39
8	136	((digital.ti. and analog.ti.). and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:39
9	71	((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider)) not us.cc.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:55
10	1	"5739727".PN.	USPAT	2002/12/07 09:45
11	6	(("4490688") or ("5057793") or ("5978425")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:56
12	125	(digital.ti. and analog.ti.) and (PLL.ti. or (phase.ti. adj2 loop))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:57
13	94	((digital.ti. and analog.ti.) and (PLL.ti. or (phase.ti. adj2 loop))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider)) not us.cc.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:57
14	49	((((digital.ti. and analog.ti.) and (PLL.ti. or (phase.ti. adj2 loop))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider)) not us.cc.)) not us.cc.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 09:59

15	1644	((digital and analog) and (phase adj2 loop.ab.)) and lock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:03
16	426	((digital and analog) and (phase adj2 loop.ab.)) and lock) and (digital.ab. and analog.ab.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:04
17	412	((((digital and analog) and (phase adj2 loop.ab.)) and lock) and (digital.ab. and analog.ab.)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:04
18	360	((((digital and analog) and (phase adj2 loop.ab.)) and lock) and (digital.ab. and analog.ab.)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:04
19	360	((((((digital and analog) and (phase adj2 loop.ab.)) and lock) and (digital.ab. and analog.ab.)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not (((digital.ti. and analog.ti.) and (PLL.ti. or (phase.ti. adj2 loop))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not (((digital.ti. and analog.ti.) and (PLL.ti. or (phase.ti. adj2 loop))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not us.cc.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:04
20	224	(((((((digital and analog) and (phase adj2 loop.ab.)) and lock) and (digital.ab. and analog.ab.)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not (((digital.ti. and analog.ti.) and (PLL.ti. or (phase.ti. adj2 loop))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not (((digital.ti. and analog.ti.) and (PLL.ti. or (phase.ti. adj2 loop))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not us.cc.)) not us.cc.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:05

21	50	(((((digital and analog) and (phase adj2 loop.ab.)) and lock) and (digital.ab. and analog.ab.)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting)))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not (((digital.ti. and analog.ti.) and (PLL.ti. or (phase.ti. adj2 loop))) not (((digital.ti. and analog.ti.) and (phase.ab. adj2 loop)) not (((digital and analog) and (phase adj2 loop.ab.)) and (lock adj (detector or detecting))) and (counter or divider))) not us.cc.)) not us.cc.) and jitter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:07
22	3		USPAT	2002/12/07 10:07
23	607	((digital and analog) and (phase adj2 loop.ab.)) and jitter ("4180783" "4313209" "4577163").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:07
24	533	((digital and analog) and (phase adj2 loop.ab.)) and jitter) not us.cc.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:07
25	393	((((digital and analog) and (phase adj2 loop.ab.)) and jitter) not us.cc.) and lock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/12/07 10:08

09889260_CLSTITLES

Titles of Most Frequently Occurring Classifications of Patents Returned
From A Search of 09889260 on March 05, 2002

09/889260

- 19 331/1A (6 OR, 13 XR)
 - Class 331 : OSCILLATORS
 - 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE OR FREQUENCY SENSING MEANS
 - 331/1A .AFC with logic elements
- 19 375/376 (4 OR, 15 XR)
 - Class 375 : PULSE OR DIGITAL COMMUNICATIONS
 - 375/354 SYNCHRONIZERS
 - 375/371 .Phase displacement, slip or jitter correction
 - 375/373 ..Phase locking
 - 375/376 ...Phase locked loop
- 16 331/17 (1 OR, 15 XR)
 - Class 331 : OSCILLATORS
 - 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE OR FREQUENCY SENSING MEANS
 - 331/17 .Particular error voltage control (e.g., intergrating network)
- 15 331/25 (1 OR, 14 XR)
 - Class 331 : OSCILLATORS
 - 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE OR FREQUENCY SENSING MEANS
 - 331/18 .With reference oscillator or source
 - 331/25 ..Signal or phase comparator
- 9 327/156 (1 OR, 8 XR)
 - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
 - 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
 - 327/141 .Synchronizing
 - 327/155 ..With feedback
 - 327/156 ...Phase lock loop
- 9 331/11 (6 OR, 3 XR)
 - Class 331 : OSCILLATORS
 - 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE OR FREQUENCY SENSING MEANS
 - 331/10 .Plural A.F.S. for a single oscillator
 - 331/11 ..Plural comparators or discriminators
- 8 327/157 (4 OR, 4 XR)
 - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
 - 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
 - 327/141 .Synchronizing
 - 327/155 ..With feedback
 - 327/156 ...Phase lock loop
 - 327/157With charge pump
- 7 327/159 (0 OR, 7 XR)
 - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
 - 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
 - 327/141 .Synchronizing

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- 327/155 ..With feedback
- 327/156 ...Phase lock loop
- 327/159With digital element

- 6 375/371 (0 OR, 6 XR)
 - Class 375 : PULSE OR DIGITAL COMMUNICATIONS
 - 375/354 SYNCHRONIZERS
 - 375/371 .Phase displacement, slip or jitter correction

- 6 375/374 (4 OR, 2 XR)
 - Class 375 : PULSE OR DIGITAL COMMUNICATIONS
 - 375/354 SYNCHRONIZERS
 - 375/371 .Phase displacement, slip or jitter correction

 - 375/373 ..Phase locking
 - 375/374 ...With charge pump or up and down counters

- 5 375/375 (1 OR, 4 XR)
 - Class 375 : PULSE OR DIGITAL COMMUNICATIONS
 - 375/354 SYNCHRONIZERS
 - 375/371 .Phase displacement, slip or jitter correction

 - 375/373 ..Phase locking
 - 375/375 ...With frequency detector and phase detector

- 4 331/12 (0 OR, 4 XR)
 - Class 331 : OSCILLATORS
 - 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE OR FREQUENCY SENSING MEANS
 - 331/10 .Plural A.F.S. for a single oscillator
 - 331/11 ..Plural comparators or discriminators
 - 331/12 ...With phase-shifted inputs

- 4 331/14 (2 OR, 2 XR)
 - Class 331 : OSCILLATORS
 - 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE OR FREQUENCY SENSING MEANS
 - 331/14 .With intermittent comparison controls

- 4 375/373 (1 OR, 3 XR)
 - Class 375 : PULSE OR DIGITAL COMMUNICATIONS
 - 375/354 SYNCHRONIZERS
 - 375/371 .Phase displacement, slip or jitter correction

 - 375/373 ..Phase locking

- 3 327/12 (0 OR, 3 XR)
 - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
 - 327/1 SPECIFIC SIGNAL DISCRIMINATING (E.G.,
COMPARING, SELECTING, ETC.) WITHOUT SUBSEQUENT CONTROL
 - 327/2 .By phase
 - 327/3 ..Comparison between plural inputs (e.g., phase angle indication, lead-lag discriminator, etc.)
 - 327/12 ...With logic or bistable circuit

- 3 327/147 (0 OR, 3 XR)
 - Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS

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- 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
- 327/141 .Synchronizing
- 327/144 ..Using multiple clocks
- 327/146 ..With feedback
- 327/147Phase lock loop

3 327/160 (0 OR, 3 XR)

- Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
- 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
- 327/141 .Synchronizing
- 327/160 ..With counter

3 327/7 (1 OR, 2 XR)

- Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
- 327/1 SPECIFIC SIGNAL DISCRIMINATING (E.G., COMPARING, SELECTING, ETC.) WITHOUT SUBSEQUENT CONTROL
- 327/2 .By phase
- 327/3 ..Comparison between plural inputs (e.g., phase angle indication, lead-lag discriminator, etc.)
- 327/7 ...With reference signal

3 331/8 (1 OR, 2 XR)

- Class 331 : OSCILLATORS
- 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE OR FREQUENCY SENSING MEANS
- 331/8 .Transistorized controls

3 331/DIG 2 (0 OR, 3 XR)

- Class 331 : OSCILLATORS
- 331/DIG 2 Phase locked loop having lock indicating or detecting means

2 327/158 (1 OR, 1 XR)

- Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
- 327/100 SIGNAL CONVERTING, SHAPING, OR GENERATING
- 327/141 .Synchronizing
- 327/155 ..With feedback
- 327/156 ...Phase lock loop
- 327/158With variable delay means

2 327/536 (0 OR, 2 XR)

- Class 327 : MISCELLANEOUS ACTIVE ELECTRICAL NONLINEAR DEVICES, CIRCUITS, AND SYSTEMS
- 327/524 SPECIFIC IDENTIFIABLE DEVICE, CIRCUIT, OR SYSTEM
- 327/530 .With specific source of supply or bias voltage

327/534 ..Having particular substrate biasing

327/535 ...Having stabilized bias or power supply level

327/536Charge pump details

2 331/16 (0 OR, 2 XR)

- Class 331 : OSCILLATORS
- 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE OR FREQUENCY SENSING MEANS
- 331/16 .Tuning compensation

2 331/177R (0 OR, 2 XR)
 Class 331 : OSCILLATORS
 331/177R WITH FREQUENCY ADJUSTING MEANS

2 331/18 (0 OR, 2 XR)
 Class 331 : OSCILLATORS
 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE
 OR FREQUENCY SENSING MEANS
 331/18 .With reference oscillator or source

2 331/27 (0 OR, 2 XR)
 Class 331 : OSCILLATORS
 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE
 OR FREQUENCY SENSING MEANS
 331/18 .With reference oscillator or source
 331/25 ..Signal or phase comparator
 331/27 ...Plural active element (e.g., triodes)

2 331/34 (0 OR, 2 XR)
 Class 331 : OSCILLATORS
 331/1R AUTOMATIC FREQUENCY STABILIZATION USING A PHASE
 OR FREQUENCY SENSING MEANS
 331/34 .Particular frequency control means

2 331/57 (2 OR, 0 XR)
 Class 331 : OSCILLATORS
 331/57 RING OSCILLATORS

2 348/537 (1 OR, 1 XR)
 Class 348 : TELEVISION
 348/500 SYNCHRONIZATION
 348/536 .Automatic phase or frequency control
 348/537 ..Of sampling or clock

2 348/540 (0 OR, 2 XR)
 Class 348 : TELEVISION
 348/500 SYNCHRONIZATION
 348/536 .Automatic phase or frequency control
 348/540 ..Horizontal sync component

2 348/542 (0 OR, 2 XR)
 Class 348 : TELEVISION
 348/500 SYNCHRONIZATION
 348/536 .Automatic phase or frequency control
 348/540 ..Horizontal sync component
 348/542 ...Plural distinct operating modes

2 375/226 (0 OR, 2 XR)
 Class 375 : PULSE OR DIGITAL COMMUNICATIONS
 375/224 TESTING
 375/226 .Phase error or phase jitter

2 386/85 (1 OR, 1 XR)
 Class 386 : TELEVISION SIGNAL PROCESSING FOR DYNAMIC
 RECORDING OR REPRODUCING
 386/46 PROCESSING OF TELEVISION SIGNAL FOR DYNAMIC
 RECORDING OR REPRODUCING
 386/85 .Time (e.g., phase or frequency) correction

2 702/69 (1 OR, 1 XR)
 Class 702 : DATA PROCESSING: MEASURING, CALIBRATING, OR

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TESTING

702/1 MEASUREMENT SYSTEM IN A SPECIFIC ENVIRONMENT

702/57 .Electrical signal parameter measurement system

702/66 ..Waveform analysis

702/69 ...Signal quality (e.g., timing jitter,
distortion, signal-to-noise ratio)